

Claim 2. (Canceled) The improvement of claim 1 wherein said second portion of said interface is at least one contacting area positioned orthogonally with respect to said aligned direction.

Claim 3. (Canceled) The improvement of claim 1 wherein said second portion of said interface is an elongated contact in contact with said surface and a circular contact in contact with said wiring support member for each member of said array.

Claim 4. (Currently amended) The improvement of claim ~~1~~ 19 wherein said second portion of said interface is a contacting area taken from the group of:
contact areas to the surface to which said conductive joints are attached,
alternate conductive joint members attached to said wiring support member; and,
elongated and circular contacts at opposite ends of each conductive joint with said
elongated contact at said surface and said circular contact in contact with said wiring support.

Claim 5. (Currently amended) The improvement of claim ~~1~~ 19 wherein said second portion of said interface is an elongated contact in contact with said surface having major and minor essentially perpendicular axes and a circular contact having a radius in contact with said wiring support member for each member of said array.

Claim 6. (Previously presented) The improvement of claim 5 wherein the bending stress resistant value of said second portion of said interface is a ratio of said radius value over said minor axis value.

Claim 7. (Canceled) An improvement in an array of conductive joints between pads on a surface of an integrated circuit member of a material having a first thermal expansion responsiveness and corresponding contacts on an aligned wiring support member of a material having a second thermal responsiveness, comprising in combination: an interface between said pads and said contacts, having first and second portions, said first portion of said interface containing an array of elongated conductive joint members each having a contacting area made up of a length contacting dimension and a width contacting dimension and with said length contacting dimension being longer than said width dimension, said array of conductive joint members each being oriented with said length contacting length dimension in a common direction, and, said second portion of said interface having a contacting area approximating the contacting area of said conductive joint members of said first portion and so positioned to accommodate expansion mismatch stresses in said conductive joint members.

Claim 8. (Canceled) The improvement of claim 7 wherein said second portion of said interface is at least one contacting area positioned orthogonally with respect to said common direction.

Claim 9. (Canceled) The improvement of claim 7 wherein said second portion of said interface is an elongated contact in contact with said surface and a circular contact in contact with said wiring support member for each member of said array.

Claim 10. (Currently amended) The improvement of claim 9 20 wherein said second portion of said interface is a contacting area taken from the group of: contact areas to the surface to which said conductive joints are attached, alternate conductive joint members attached to said wiring support member; and, elongated and circular contacts at opposite ends of each conductive joint with said elongated contact at said surface and said circular contact in contact with said wiring support.

Claim 11. (Previously presented) The improvement of claim 10 wherein said second portion of said interface is an elongated contact in contact with said surface having major and minor essentially perpendicular axes and a circular contact having a radius in contact with said wiring support member for each member of said array.

Claim 12. (Previously presented) The improvement of claim 11 wherein the bending stress resistant value of said second portion of said interface is a ratio of said radius value over said minor axis value.

Claim 13. (~~Non-elected~~Withdrawn) The process of forming an array of conductive joint connections between signal pads on a surface of an integrated circuit member of a material having a first thermal expansion responsiveness and corresponding contacts on an aligned wiring support member of a material having a second thermal responsiveness, comprising in combination the steps of: forming a selected thickness screen stencil of selected length and width approximately half length dimensions for measured volume openings oriented along said length for each pad and contact combination member of said array, positioning said screen stencil over said integrated circuit, in registration with said orientation of said pads wiping a slurry of particles of a low temperature fusible material in a fluid in the direction of said long dimension of said openings, across, into and filling said openings in said screen stencil, removing said screen stencil, leaving measured volumes of said selected length and width alignment and with selected orientation on said integrated circuit, positioning said printed circuit over said measured volumes in registration with said pads and, fusing each said measured volume of said low temperature fusible material to a respective pad and contact combination.

Claim 14. (~~Non-elected~~Withdrawn) The process of claim 13 wherein said low temperature fusible material is solder.

Claim 15. (~~Non-elected~~Withdrawn) The process of claim 14 wherein each pad and contact combination has an elongated shape.

Claim 16. (~~Non-elected~~Withdrawn) The process of claim 15 wherein in said forming step said openings are selected to have one of said length and width dimensions aligned with a wipe direction from side to side of said array.

Claim 17. (~~Non-elected~~Withdrawn) The process of claim 16 wherein in said forming step said openings are selected to have one of said length and width dimensions aligned with a wipe direction from center to side of said array.

Claim 18. (~~Non-elected~~Withdrawn) The process of claim 17 wherein in said forming step said openings are selected to have one of said length and width dimensions aligned with the direction of greatest thermal cycling expansion mismatch movement.

Claim 19 (New) In an array of conductive joints between signal pads on a surface of an integrated circuit member of a material having a first thermal responsiveness and corresponding contacts on an aligned wiring support member of a material having a second thermal responsiveness,
the improvement comprising:
an interface having first and second portions,
said first portion of said interface containing an array of elongated conductive joint members, each having a contacting area made up of a length contacting dimension and a width contacting dimension and with said length contacting dimension being longer than said width dimension, and,
said second portion of said interface having a contacting area approximating the contacting area of said conductive joint members of said first portion, being so positioned to accommodate expansion mismatch stresses in said conductive joint members, being at least one contacting area positioned orthogonally with respect to said common aligned direction, and being an elongated contact in contact with said surface and a circular contact in contact with said wiring support for each member of said array.

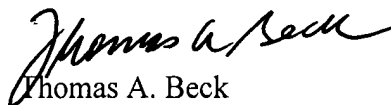
Claim 20. (New) An improvement in an array of conductive joints between signal pads on a surface of an integrated circuit member of a material having a first thermal responsiveness and corresponding contacts on an aligned wiring support member of a material having a second thermal responsiveness,

Comprising in combination:

an interface between said pads and said contacts having first and second portions,
said first portion of said interface containing an array of elongated conductive joint members, each having a contacting area made up of a length contacting dimension and a width contacting dimension and with said length contacting dimension being longer than said width dimension, Said array of conductive joint members being oriented with said length contacting length dimension in a common direction, and,
said second portion of said interface having a contacting area approximating the contacting area of said conductive joint members of said first portion, being so positioned to accommodate expansion mismatch stresses in said conductive joint members, being at least one contacting area positioned orthogonally with respect to said common direction, and is an elongated contact in contact with said surface and a circular contact in contact with said wiring support for each member of said array.

A power of attorney on behalf of the undersigned will be filed under separate cover. Please address all further correspondence to the undersigned at the address listed below.

Respectfully Submitted,



Thomas A. Beck
Registration Number 20,816
26 Rockledge Lane
New Milford, CT 06776

I hereby certify that this paper is being deposited on the date indicated below with the United States Postal Service as First Class Mail addressed to the Commissioner of Patents and Trademarks, Post Office Box 1450, Alexandria, VA 22313-1450

Signature  Date October 27, 2004
Thomas A. Beck